

400 Watts - 52 Volts, 100 *μ* s, 10% S-Band Radar 2700 - 3100 MHz

GENERAL DESCRIPTION

For S-band pulsed radar applications, with typically over 10.5dB gain, the 2731GN-400V is an internally matched, common source, class AB, GaN on SiC HEMT transistor capable of delivering more than 400 Watts of pulsed RF output power under 100uS, 10% pulsing across the 2700 to 3100 MHz band. Proprietary state of the art GaN on SiC semiconductor technology, internal pre-matching, hermetic seal, all gold metallization, and eutectic attachment result in a device that delivers the highest reliability and excellent ruggedness while making the 2731GN-400V the best choice to gain superior performance in the most demanding system designs.

55-QP
Common Source

ABSOLUTE MAXIMUM RATINGS

Maximum Power Dissipation

Device Dissipation @ 25°C 1170 W

Maximum Voltage and Current

Drain-Source Voltage (V_{DSS}) 150 V Gate-Source Voltage (V_{GS}) -8 to +0 V

Maximum Temperatures

Storage Temperature (T_{STG}) -55 to +125 °C Operating Junction Temperature +250 °C



ELECTRICAL CHARACTERISTICS @ 25°C

Symbol	Characteristics Test Conditions		Min	Тур	Max	Units
Pout	Output Power	Pin=36W Freq=2700,2900,3100 MHz	400	415		W
Gp	Power Gain	Pin=36W Freq=2700,2900,3100 MHz	10.5	10.6		dB
ηd	Drain Efficiency	Pin=36W Freq=2700,2900,3100 MHz	37	40		%
Dr	Droop	Pin=36W Freq=2700,2900,3100 MHz			1.0	dB
VSWR-T	Load Mismatch Tolerance	Pout=400W, Freq= 2700MHz			3:1	
Өјс	Thermal Resistance	Pulse Width=100uS, Duty=10%			0.18	°C/W

Bias Condition: Vdd=+52V, Idq=100mA constant current (Vgs= -2.0 ~ -4.5V typical)

FUNCTIONAL CHARACTERISTICS @ 25°C

I _{D(Off)}	Drain leakage current	$V_{gs} = -8V, V_D = 150V$		64	mA
$I_{G(Off)}$	Gate leakage current	$V_{gs} = -8V, V_D = 0V$		20	mA
BV _{DSS}	Drain-source breakdown voltage	V_{gs} =-8V, I_D = 64mA	150		V

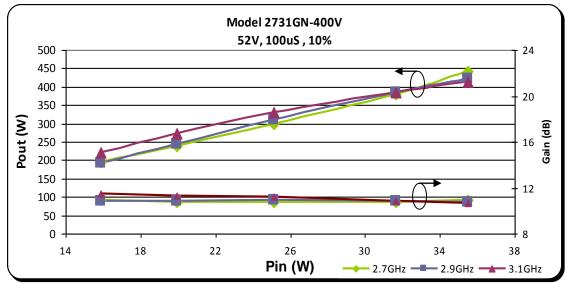
Export Classification: EAR-99

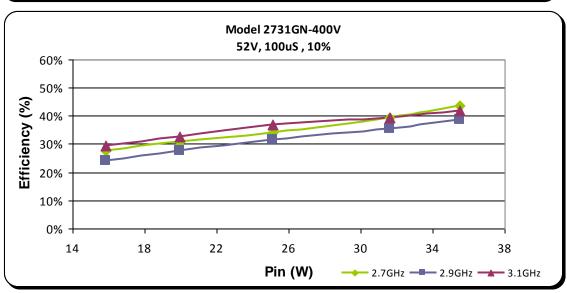


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TYPICAL BROAD BAND PERFORMACE DATA

Frequency	Pin (W)	Pout (W)	ld (A)	RL (dB)	ηd (%)	Gp (dB)	Droop (dB)
2700 MHz	36	442	2.04	-7.2	44	10.95	0.6
2900 MHz	36	422	2.20	-15.5	39	10.75	0.5
3100 MHz	36	415	2.01	-9.2	42	10.68	0.4



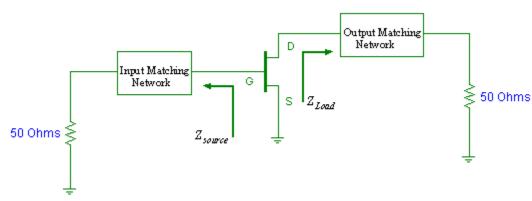


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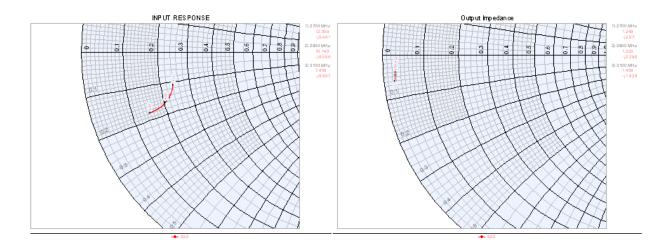
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TRANSISTOR IMPEDANCE INFORMATION



Note: Z_{source} is looking into the input circuit; Z_{load} is looking into the output circuit.

Impedance Data					
Freq (GHz)	Z _{source}	Z _{load}			
2.7	12.06 – j5.44	1.25 – j2.97			
2.9	10.14 – j8.09	1.33 – j2.29			
3.1	7.40 – j8.88	1.40 – j1.92			

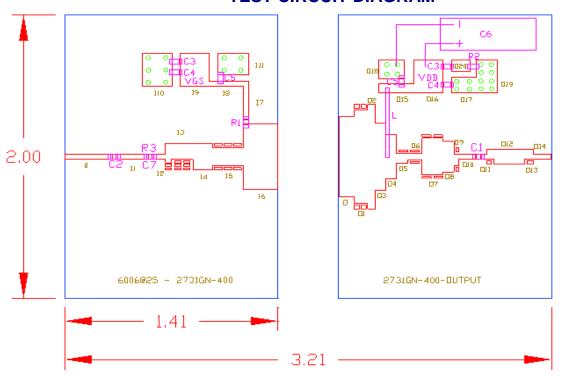


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TEST CIRCUIT DIAGRAM



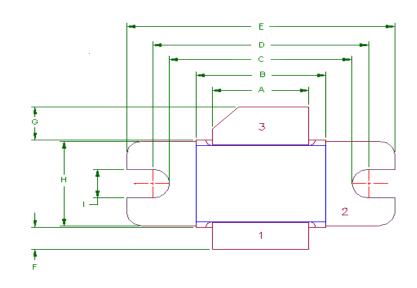
Board Material: Roger Duriod 6006 @ 25 Mil Thickness, Er=6.15

	Component List			hysical Circ	cuit Layout	Output Physical Circuit Layout		Output Physical Circuit Layout			
Item	Description	Value	Item	W (mil)	L (mil)	Item	W (mil)	L (mil)	Item	W (mil)	L (mil)
C1	Chip Cap A size	9.1pF	_	35	312	0	570	99	012	100	250
C2	Chip Cap A size	9.1pF	11	35	210	01	660	89	013	68	57
C3	Chip Cap B size	10,000pF	12	35	90	02	736	50	014	35	118
C4	Chip Cap B size	1000pF	13	113	182	03	476	70	015	64	230
C5	Chip Cap B size	100pF	14	190	128	04	310	70	016	230	180
C10	Electrolytic Cap (63V)	4700uF	15	130	204	05	71	75	017	110	160
R1	Chip Resistor size 0805	11.5 ohms	16	460	229	-06	45	95	018	135	170
R2	Chip Resistor size 0805	2 Ohms	17	35	250	07	260	134	019	230	140
R3	Chip Resistor size 0805	523 ohms	18	35	245	08	215	74	020	80	124
C7	Chip Cap A size	6.8pF	19	230	190	09	82	34			
L	L Cu wire 20AWG, L=670 mil		l10	230	200	010	35	115			
Board:	Board: Duroid 6006 - 25 Mil Thick - Er = 6.15			150	206	011	35	42			



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55-QP PACKAGE DIMENSION







1	=	Gate
2	=	Source
3	=	Drain

Dimension	Min (mil)	Min (mm)	Max (mil)	Max (mm)
Α	370	9.40	372	9.44
В	498	12.65	500	12.7
С	700	17.78	702	17.83
D	830	21.08	832	21.13
E	1030	26.16	1032	26.21
F	101	2.56	102	2.59
G	151	3.84	152	3.86
Н	385	9.78	387	9.83
I	130	3.30	132	3.35
J	003	.076	004	0.10
K	135	3.43	137	3.48
L	105	2.67	107	2.72
M	085	2.16	86	2.18
N	065	1.65	66	1.68



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Revision History

Revision Level / Date	Details
0.2 / 08-30-13	52V, 100us, 10% Initial Preliminary Release

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